
MCP215X Rev. B Silicon/Data Sheet Errata

The MCP215X parts you have received conform functionally to the MCP2150 and MCP2155 device data sheets (DS21655B and DS21690A), except for the anomalies described below.

MCP215X Rev. C devices have addressed these issues.

1. Module: IR Protocol Handler

The MCP215X device will ignore queries from the Primary device (PC, PDA) for the status of the non-data circuits (CTS, RTS, DTR, DSR, CD, RI).

Work around

Do not have the Primary device application request these signals for application operation.

2. Module: IR Protocol Handler

Some new Palm® PDA devices use a processor (TI OMAP31X) that sends a non-standard number of BOFs (4) prior to parameter negotiation. The MCP215X device expects 11 BOFs.

When the Primary device sends the SNRM command (including the requested number of BOFs (1) to use), the MCP215X responds with a UA response using this number of BOFs (1). Since the response only contains a single BOF, the TI OMAP31X processors ignore this response. Therefore, the communication link with the Primary device is not established.

Work around

Do not use Palm PDAs that use the TI (OMAP310 and OMAP311) processor. Some of the Palm PDAs that have this processor include the Tungsten™ E, Zire™ 71 and Zire 21.

The Tungsten T2 (TI OMAP1510 with OS V5.2.1) does not appear to have a connection issue with the MCP215X device and, therefore, can be used to establish a communication link with MCP215X devices.

The MCP2140 could be used, since it can connect to Palm PDAs that use this TI (OMAP310 and OMAP311) processor.

3. Module: IR Protocol Handler

The MCP215X IAS parser will supply LSAPSEL response for more than just an IrDA:IrCOMM LSAPSEL query. Some models of Pocket PC 2003 devices will issue an IrDA:IrObex LSAPSEL query once the Pocket PC 2003 device has been informed that its communicating with an IrCOMM device. This causes the Ir link to not be established.

Work around

None for the MCP215X. However, the MCP2140 could be used and will respond to this Pocket PC 2003 IrDA:IrObex LSAPSEL query, allowing the Ir link to be established.

4. Module: IR Protocol Handler

The MCP215X may supply an RR response instead of a TinyTP Credit response to a received data packet.

Work around

None.

<p>Note: Currently, we have not observed any loss of data or loss of communication link when interfacing to other existing IrDA® standard protocol stacks due to this issue.</p>

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Clarifications/Corrections to the Data Sheet:

In the MCP2150 and MCP2155 device data sheets (DS21655B and DS21690A), the following clarifications and corrections should be noted:

1. An IrDA standard operating system (Symbian OS™ for cell phones) has been encountered that only sends out XID commands with a Timeslot of 0. Since the MCP215X devices respond to Timeslot 2, MCP215X devices are not able to establish a link to an OS that is compliant with the IrDA standard. The MCP2140 can be used in these systems, since it responds to the XIP command with Timeslot 0.
2. The MCP2150 emulates the state of all the non-data circuits (CTS, RTS, DTR, DSR, CD and RI). These signals on the MCP2150 are used to interface with the embedded system's host controller. The values in the IrDA standard packet response would not reflect the value on the corresponding MCP2150 pin.

The MCP2155 emulates the state of the CTS, RTS and DSR non-data circuits. These signals on the MCP2155 are used to interface with the embedded system's host controller. The DTR, CD and RI signal values are communicated to the IrDA standard Primary device in an IrCOMM packet. The MCP2155 will only send this information when the state of one of these signals change. The MCP2155 devices will ignore any request for the status of the non-data circuits from the IrDA standard Primary device.

APPENDIX A: REVISION HISTORY

Revision A (February 2006)

- Initial release of this document.

APPENDIX B: SILICON REVISION HISTORY

The following table and package marking information illustrates how to determine the revision of the MCP215X device. The revision information can be determined by the Year and Week Code of the manufacture printed on the device.

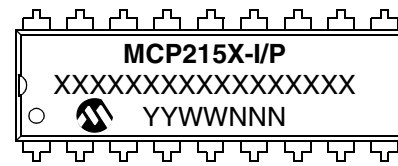
TABLE B-1: SILICON REVISION/DEVICE MARKING

Silicon Revision	YYWWNNN		Comments
	Start Date	End Date	
Rev. C	0419NNN	—	In Production
Rev. B	0231NNN	0418NNN	

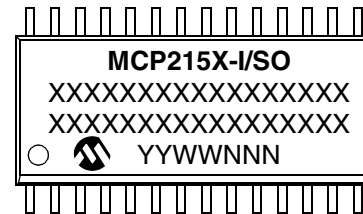
Legend: "N" is any alphanumeric character.

Package Marking Information

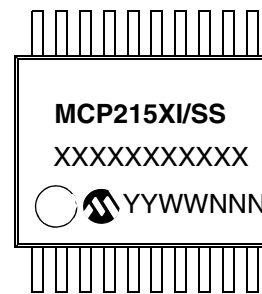
18-Lead PDIP



18-Lead SOIC



20-Lead SSOP



Legend: XX...X Customer specific information*

YY Year code
(last 2 digits of calendar year)
WW Week code
(week of January 1 is week '01')
NNN Alphanumeric traceability code

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line thus limiting the number of available characters for customer specific information.

* Standard marking consists of Microchip part number, year code, week code and traceability code.

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NOTES:

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