

mikroBoard for ARM 64-pin™

User manual

All MikroElektronika's development systems represent irreplaceable tools for programming and developing microcontroller-based devices. Carefully chosen components and the use of machines of the last generation for mounting and testing thereof are the best guarantee of high reliability of our devices. Due to simple design, a large number of add-on modules and ready to use examples, all our users, regardless of their experience, have the possibility to develop their project in a fast and efficient way.

Development system

 **MikroElektronika**

SOFTWARE AND HARDWARE SOLUTIONS FOR EMBEDDED WORLD ...making it simple

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The primary aim of our company is to design and produce high quality electronic products and to constantly improve the performance thereof in order to better suit your needs.



Nebojsa Matic
General Manager

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2. LPC2148 microcontroller

The LPC2148 microcontroller in 64-pin LQFP package is soldered on the mikroBoard for ARM 64-pin. Some of its key features are:

- 16-bit/32-bit ARM7TDMI-S microcontroller in a tiny LQFP64 package
- 40 kB of on-chip static RAM and 512 kB of on-chip flash memory. 128-bit wide interface/ accelerator enables high-speed 60 MHz operation
- In-System Programming/In-Application Programming (ISP/IAP) via on-chip boot loader software. Single flash sector or full chip erase in 400 ms and programming of 256 B in 1 ms
- USB 2.0 full-speed compliant device controller with 2 kB of endpoint RAM
- Low power Real-Time Clock (RTC) with independent power and 32 kHz clock input

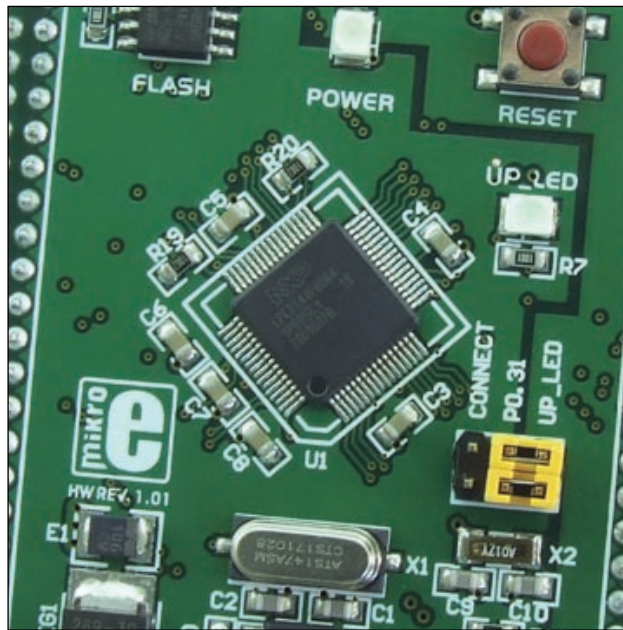


Figure 2-1: LPC2148 microcontroller

The LPC2148 is connected to on-board modules via pins which are also connected to the CN1 and CN2 connectors. These two connectors enable the board to be connected to the EasyARM v6 development system or some other device.

3. Programming the microcontroller

The microcontroller can be programmed with a bootloader or the JTAG programmer. The use of bootloader is enabled due to the bootloader code that is loaded into the microcontroller. In order to program the microcontroller with the bootloader, it is necessary to connect the board to a PC via the CN3 connector and USB cable, figure 3-1. A .hex code is transferred from the PC to the microcontroller by using some of the bootloader programs, such as Flash Magic.

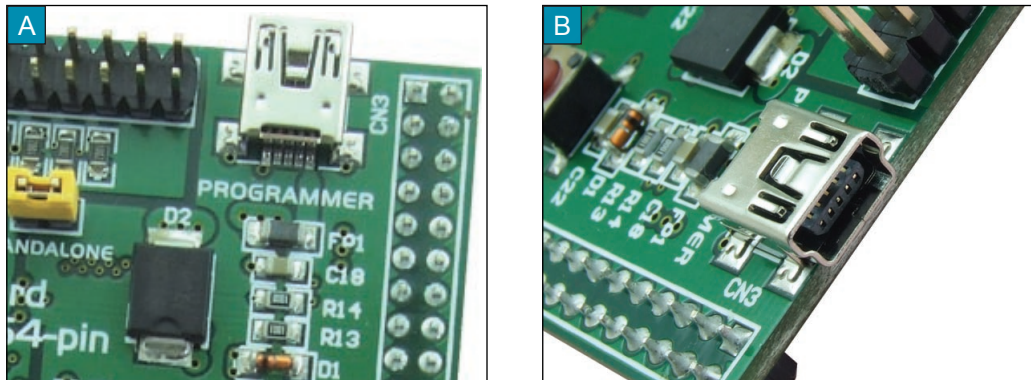


Figure 3-1: USB connector for programming

The CN3 USB connector is connected to the UART module built into the microcontroller via FTDI module (FT232RL).

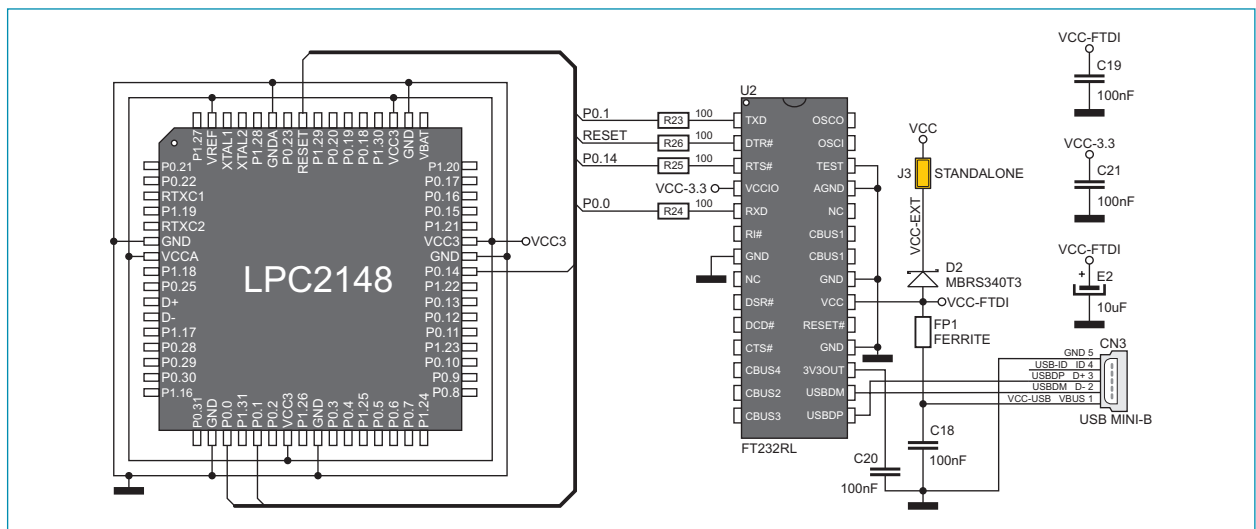
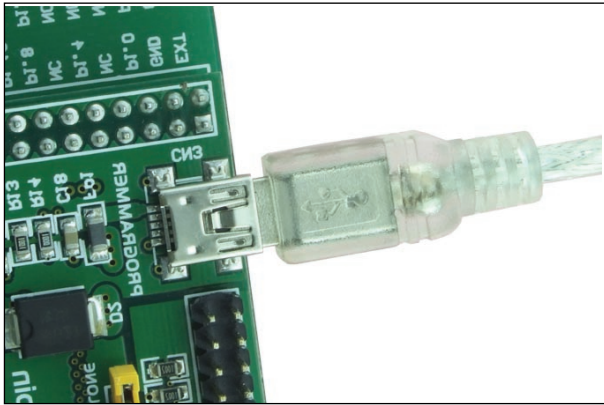


Figure 3-2: USB UART module connection schematic

When the mikroBoard for ARM 64-pin operates as a stand-alone device, it is necessary to place jumper J3 on the board. If the board is connected to the EasyARM v6 development system, jumper J3 should be removed.

The following steps explain how to program the microcontroller with bootloader via the Flash Magic application.

STEP 1: Connect the system to a PC



Connect the mikroBoard for ARM 64-pin to available USB port on your PC.

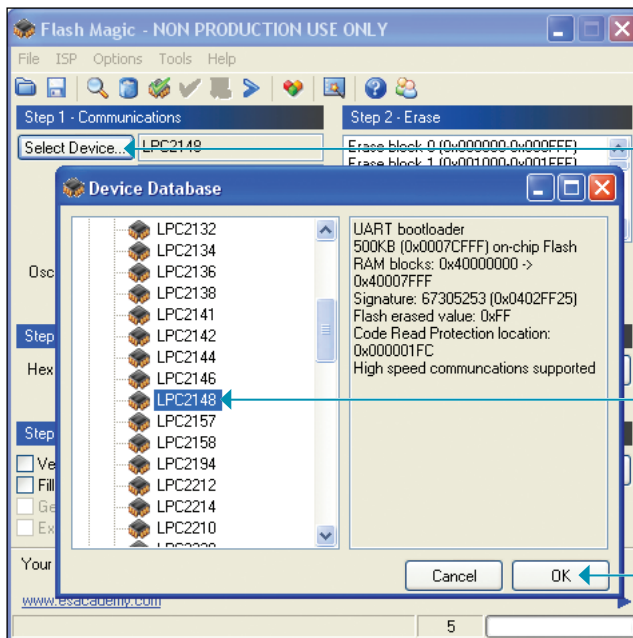
STEP 2: Start Flash Magic

Download the Flash Magic application from <http://www.flashmagictool.com/download.html&d=FlashMagic.exe> and install it on your PC.

When the installation is finished, double click on the Flash Magic icon



STEP 3: Select MCU

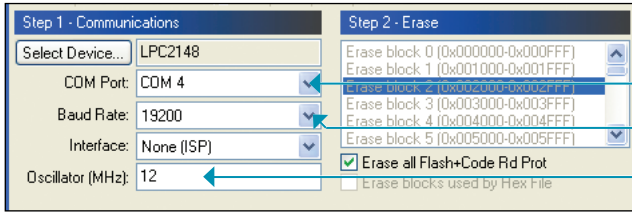


Click on the Select Device button

Select MCU from the list

Click OK

STEP 4: Settings

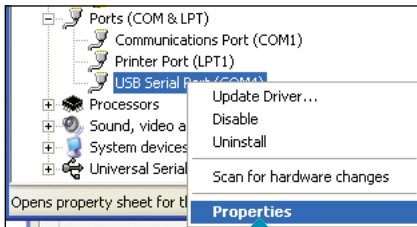


From the drop-down menu select COM port on your PC

Set Baud Rate to 19200

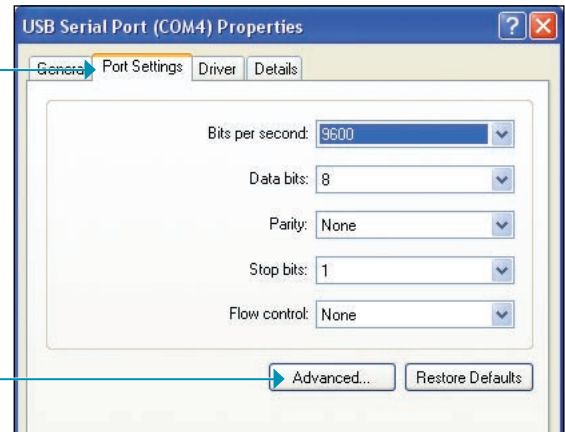
Enter 12 in the Oscillator field (if you use different oscillator enter its value in MHz instead)

Device Manager on your PC contains information on which COM port is used for USB communication with the mikroBoard for ARM 64-pin. In this case the COM4 port is used.

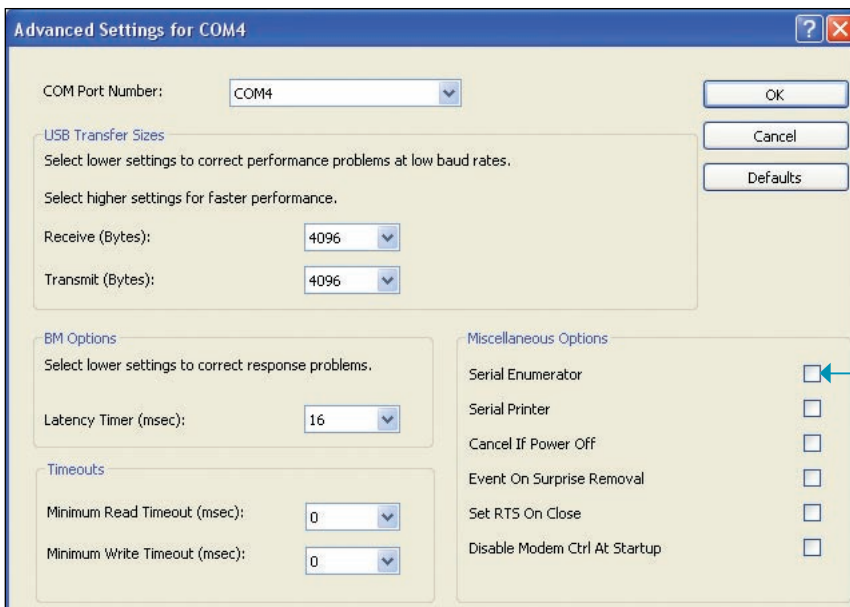


Select the Port Settings tab from pop-up window

Right click on USB port, then on properties in the drop-down list

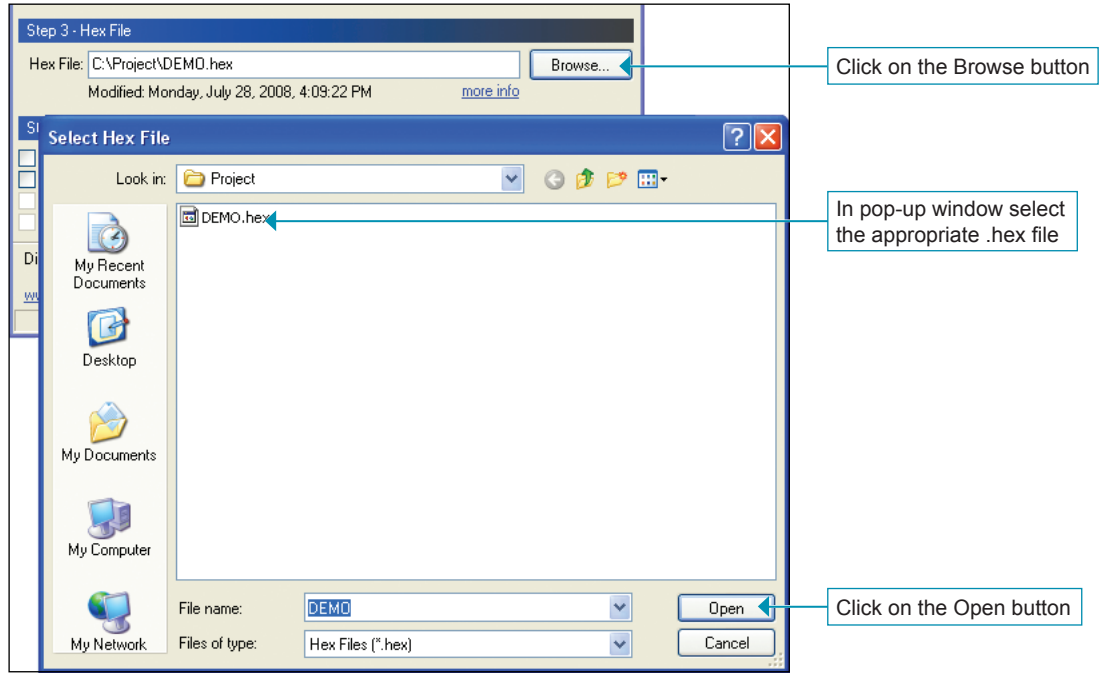


Click on the Advanced... button

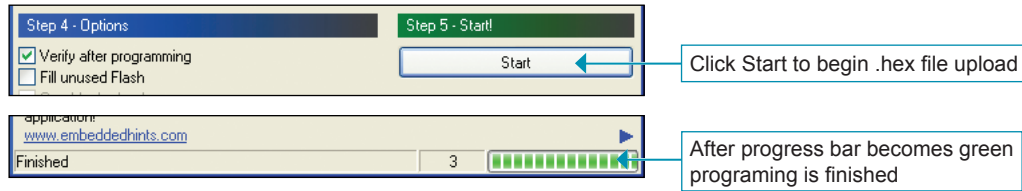


In pop-up window uncheck the Serial Enumeration option and click OK

STEP 5: Browse for .hex file



STEP 6: Upload .hex file



The microcontroller can also be programmed with the JTAG programmer, Figure 3-3. In addition, this programmer can also be used to test the operation of the microcontroller.

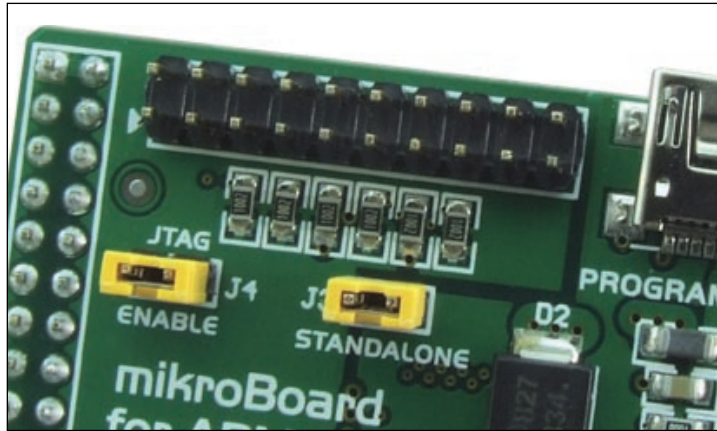


Figure 3-3: JTAG connector

In order to enable the JTAG programmer to be used, it is necessary to place jumper J4 in the ENABLE position, Figure 3-5. If the JTAG programmer is not used for programming, jumper J4 should be removed from the board, Figure 3-6.

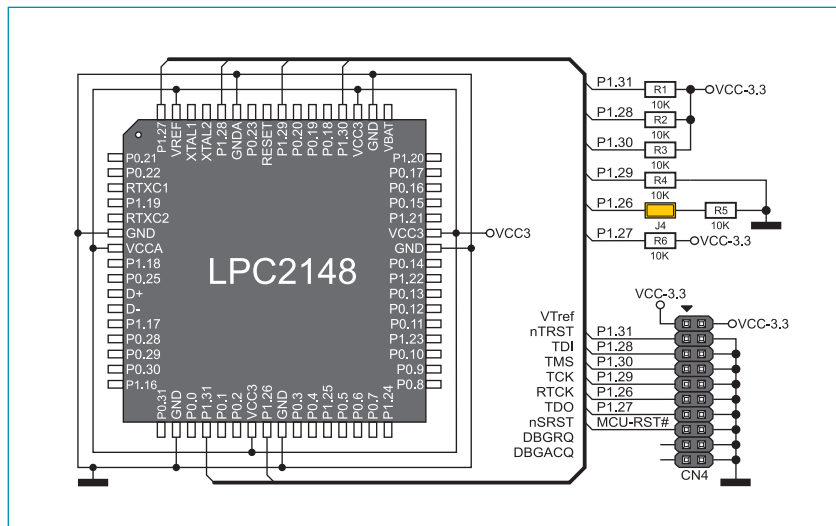


Figure 3-4: JTAG module connection schematic

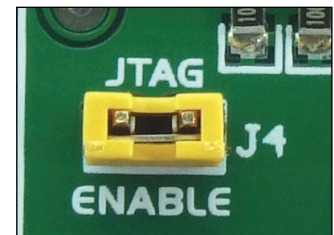


Figure 3-5: JTAG is enabled

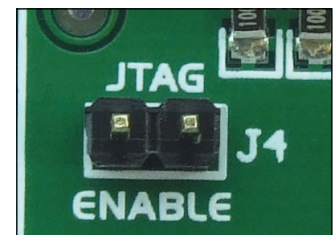


Figure 3-6: JTAG is disabled

4. Voltage regulator

The on-board microcontroller operates at 3.3V power supply voltage. The board is powered with the 5V power supply voltage via the CN3 USB connector.

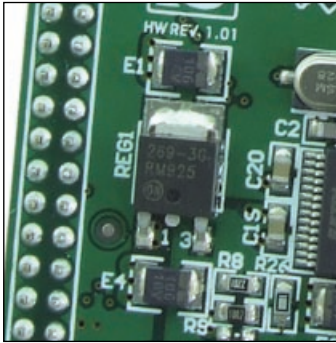


Figure 4-1: Voltage regulator

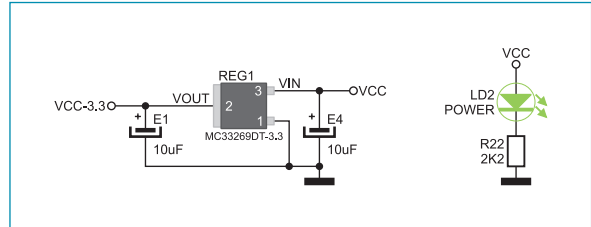


Figure 4-2: Voltage regulator connection schematic

If the board is powered by a development system (EasyARM v6), the function of the voltage regulator remains the same. In this case, it is necessary to remove jumper J3 (STANDALONE), Figure 4-3.

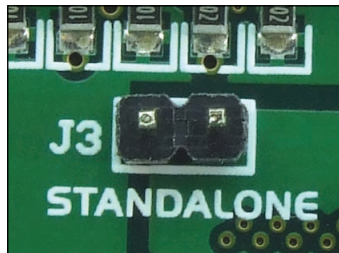


Figure 4-3: Standalone mode disabled (development system connection)

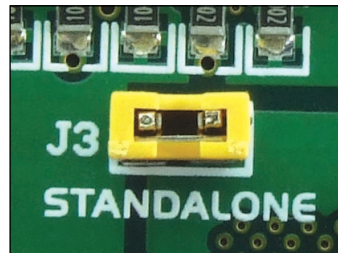


Figure 4-4: Standalone mode enabled

5. MicroSD connector

There is a connector CN5 provided on the development system that enables the use of microSD card. When inserted, the microSD card provides additional memory space that the microcontroller can use to store data. Communication between the microSD card and the microcontroller is performed via the Serial Peripheral Interface (SPI).

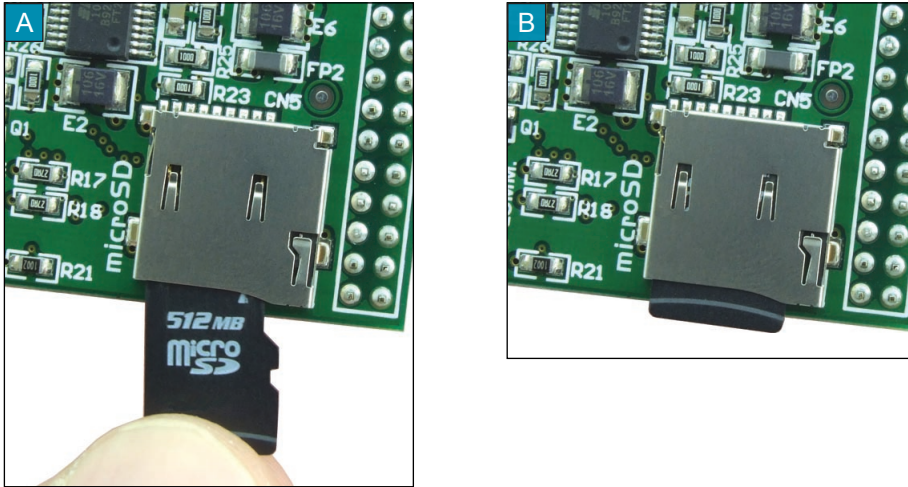


Figure 5-1: microSD connector

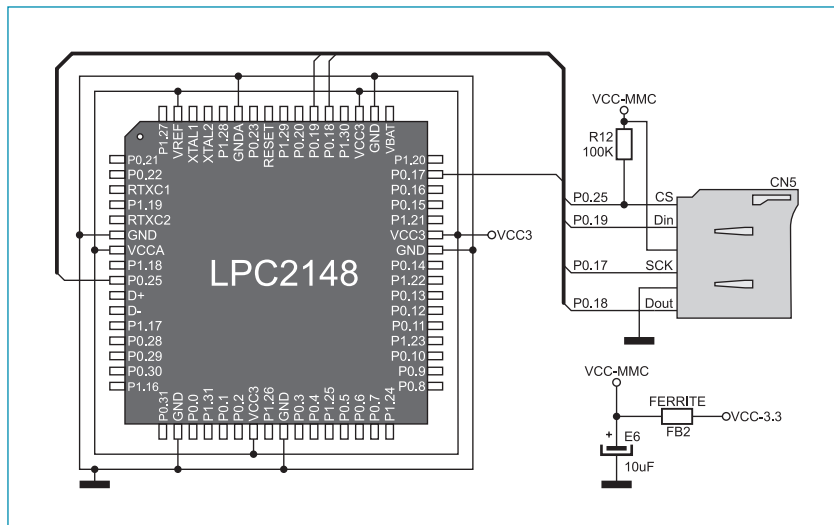


Figure 5-2: microSD connector connection schematic

The pins' designations have the following meaning:

CS - Chip Select
SCK - Clock

Din - Master Out/Slave In (MOSI)
Dout - Master In/Slave Out (MISO)

6. Flash module

Flash module provides additional 8Mbit of flash memory that the microcontroller can use via the Serial Peripheral Interface (SPI).

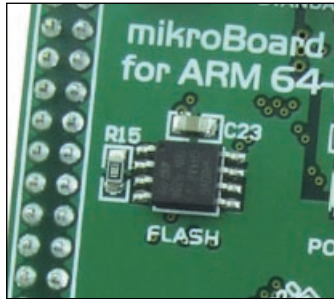


Figure 6-1: Flash memory

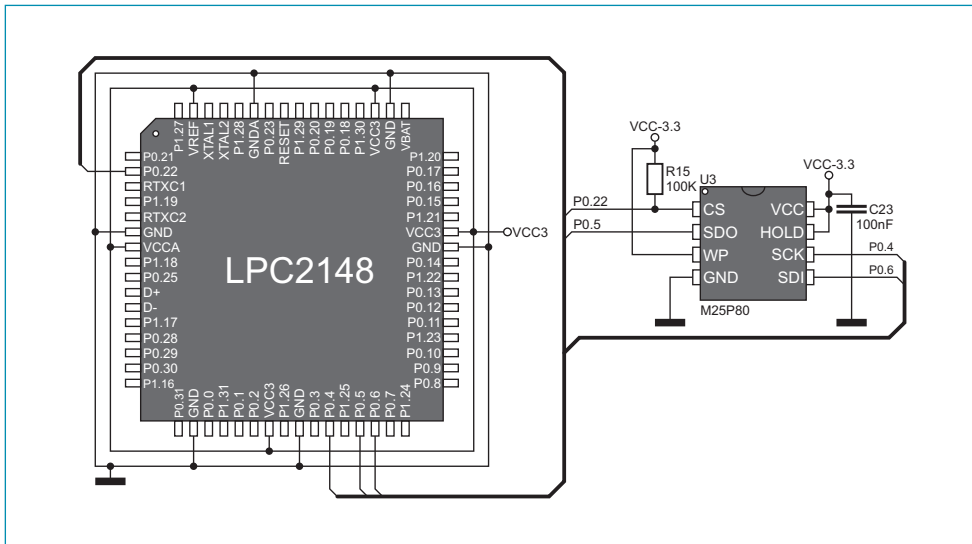


Figure 6-2: Flash module connection schematic

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